

## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University covered under Section 2(f) & 12(B) of UGC Act)

P.O: Engineering College Burla (Siddhi Vihar), Dist: Sambalpur, Odisha - 768 018, India

No.VSSUT/PGSR/235/19

Date: 22/04/19

## NOTIFICATION

## THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Ms. Sonali Dash Regn. No.14060013 is declared to be qualified for the award of the Ph.D Degree in Engineering (Electronics & TC Engineering) with effective from 22.04.2019. The title of the thesis is "TEXTURE CLASSIFICATON BY COMBINING LAWS MASK WITH TRADITIONAL TEXTURE ANALYSIS TECHNIQUES".

YEAR OF EXAMINATION - 2019
REGISTRATION NO. 14060013

By order of Vice-Chancellor

VSS University of Technolog Burla, Odisha, India-768018

Dated: 22.04.2019

Memo No. VSSUT/PGS&R/236(20)/19

## Copy forwarded to:

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- 2. Dr. Uma Ranjan Jena, Professor, Department of Electronics & TC Engineering, VSSUT, Burla Supervisor
- 3. Prof G Sasibhushana Rao, Professor of E C E, Andhra University College of Engineering, Visakhapatnam, (India) Pin-530003 (India) External Examiner, of Ph.D Open Viva-Voce.
- 4. Prof. Dr. Hossein Mousavinezhad, Professor of Electrical Engineering, Idaho State University, Pocatello, IDAHO-83209-8060, USA (External Foreign).
- 5. . All DRC Members.
- 6. HOD & Chairman, DRC (of Ms. Sonali Dash ), Department of Electronics & TC Engineering, VSSUT, Burla for information.
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